SHRI VITHAL EDUCATION & RESEARCH INSTITUTE's

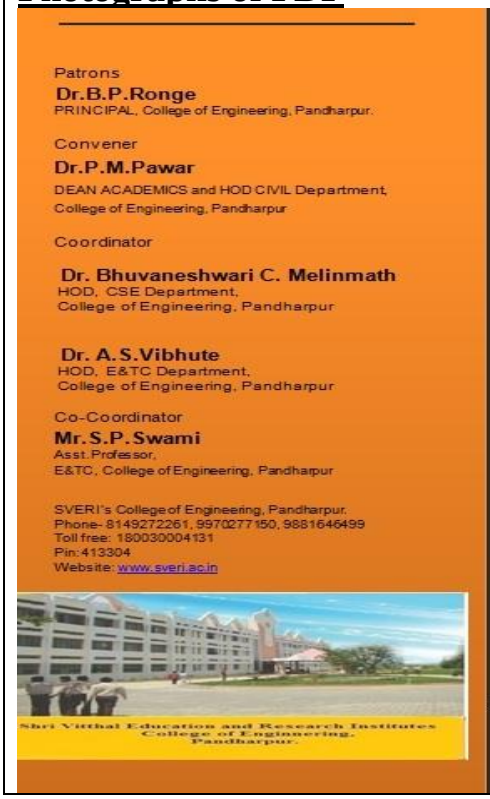
COLLEGE OF ENGINEERING, PANDHARPUR

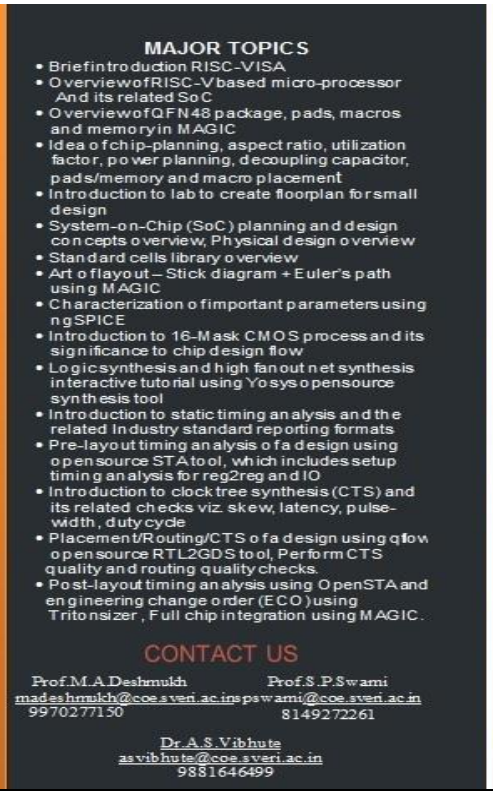
Gopalpur -Ranjani Road, Gopalpur, P.B. No. 54, Tal - Pandharpur- 413 304,
Dist. Solapur (Maharashtra) Ph.: (02186) 225083, Fax: (02186) 225082.
(Approved by AICTE, New Delhi and affiliated to Solapur University, Solapur)

FDP Report

- **Name of FDP:** VLSI Design using Open Source Methodologies and challenges in Digital Integrated Circuits and Memory Design
- **Date:** 09th Dec. to 13th Dec. 2019
- **No. of Participants**: 42
- **Brief report**: Department of CSE has organized five days faculty development program on "VLSI Design using Open Source Methodologies and challenges in Digital Integrated Circuits and Memory Design", from 09th Dec. to 13th Dec. 2019. The FDP was sponsored by IIIT DM, Jabalpur. For this FDP, total 42 faculties were participated from different institutes. Dr. Gaurav Trivedi (IIT Guwahati), Prof. Vineet Sahula (MNIT Jaipur), Dip Prakash Samajdar (IIITDM Jabalpur) and Dr. Menka Yadav (MNIT Jaipur) were resource person. This FDP was conducted in collaboration with IIITDM, Jabalpur. E&ICT Academy and funded by Ministry of Electronics and IT, Government of India.

Photographs of FDP





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Sample Attendance

Participant List
College Name SVERI's College Of Engg. Pandharpur
FDP Name with Date VLSI Chip Desion Hands On using Open source EDA 16/12/2019 to 20/12/2019

SNo	Name	Faculty/Studen/Others	Male/Female	Category(SC/ST/OBC/UR)	Institution
1	Dr. A.S.Vibhute	Faculty	Male	Open (UR)	COE,Pandharpur
2	Dr.S.R.Patil	Faculty	Male	Open (UR)	COE,Pandharpur
3	Dr.M.M.Pawar	Faculty	Female	Open (UR)	COE,Pandharpur
4	Mr. J.S.Hallur	Faculty	Male	Open (UR)	COE,Pandharpur
5	Mr.S.S.Bidwai	Faculty	Male	Open (UR)	COE,Pandharpur
6	Ms.N.P.Kulkarni	Faculty	Female	Open (UR)	COE,Pandharpur
7	Mr.M.S.Mathpati	Faculty	Male	Open (UR)	COE,Pandharpur
8	Mr.D.A.Kumbhar	Faculty	Male	OBC	COE,Pandharpur
9	Ms.G.G.Unhale	Faculty	Female	Open (UR)	COE,Pandharpur
10	Mr.S.A.Inamdar	Faculty	Male	Open (UR)	COE,Pandharpur
11	Mr.A.A.Jadhav	Faculty	Male	Open (UR)	COE,Pandharpur
12	Mr.M.A.Deshmukh	Faculty	Male	Open (UR)	COE,Pandharpur
13	Ms.S.V.Jagzap	Faculty	Female	OBC	COE,Pandharpur
14	Ms.M.Biswas	Faculty	Female	Open (UR)	COE,Pandharpur
15	Ms.S.S.Kadam	Faculty	Female	Open (UR)	COE,Pandharpur
16	Ms.S.Atole	Faculty	Female	OBC	COE,Pandharpur
17	Ms.N.S.Patil	Faculty	Female	Open (UR)	COE,Pandharpur
18	Mr.S.P.Swami	Faculty	Male	Open (UR)	COE,Pandharpur
19	Mr.V.S.Bhong	Faculty	Male	OBC	COE,Pandharpur
20	Mr.N.S.Admile	Faculty	Male	Open (UR)	COE,Pandharpur
21	Mr.A.D.Mali	Faculty	Male	OBC	COE,Pandharpur
22	Mr.D.P.Narsale	Faculty	Male	Open (UR)	COE,Pandharpur
23	Mr.A.M.Kasture	Faculty	Male	Open (UR)	COE,Pandharpur
24	Mr.A.B.Chounde	Faculty	Male	Open (UR)	COE,Pandharpur
25	Ms P B Kashid	Faculty	Female	OBC	COE,Pandharpur
26	Mr.A.A.Jadhav	Faculty	Male	Open (UR)	COE,Pandharpur
27	Ms S.R.Jadhav	Faculty	Female	Open (UR)	COE,Pandharpur
28	Ms. R. M. Shinde	Faculty	Female	Open (UR)	COE,Pandharpur
29	Mr. P.G. Gaikwad	Faculty	Male	Open (UR)	COE,Pandharpur
30	Mr. P.K.Kurzekar	Faculty	Male	OBC	COE,Pandharpur
31	Ms S.S.Bhosale	Faculty	Female	Open (UR)	COE,Pandharpur
32	Mr.R.D.Kulkarni	Faculty	Male	Open (UR)	COE,Pandharpur
33	Mr.H.M.Tamboli	Faculty	Male	OBC	COE,Pandharpur
34	Mr.S.A.Dhanave	Faculty	Male	Open (UR)	COE,Pandharpur
35	Mr.P.K.Magdum	Faculty	Male	Open (UR)	COE,Pandharpur
36	Mr.M.S.Yadrami	Faculty	Male	Open (UR)	COE,Pandharpur
37	Mr.S.C.Ukirde	Faculty	Male	Open (UR)	COE,Pandharpur
38	Ms.S.S.Yadav	Faculty	Female	Open (UR)	COE(POLY),Pandharpur
39	Ms.P.B.Maske	Faculty	Female	Open (UR)	COE(POLY),Pandharpur
40	Ms. K.I.Chouhan	Student	Female	Open (UR)	COE,Pandharpur
41	Ms. S.R.Korke	Student	Female	Open (UR)	COE,Pandharpur
42	Ms.A.S.Jadhav	Student	Female	Open (UR)	COE,Pandharpur

HOD,
Department of Computer Science & Engg.
SVERI's C.O.E. Pandharpur

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Sample Certificate


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Indian Institute of Technology Guwahati

Participation Certificate

IIT Guwahati IIITDM Jabalpur NIT Patna MNIT Jaipur SVERI's COE, Pandharpur

Mr./Ms. Shinde R.M. of ES SVERI'S COE, Pandharpur has participated in a one-week Faculty Development Programme on "VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies held from 16 – 20 December 2019, through National Knowledge Network under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology (MeitY), Government of India.

Dr. Gaurav Trivedi, Principal and Local Coordinator, Indian Institute of Technology Guwahati Assam

Prof. Vineet Sahula, Co-Principal Coordinator, Malaviya National Institute of Technology Jaipur

Dr.B. C. Melinamath, Local Coordinator, SVERI's COE, Pandharpur

Dr. Dip Prakash Samajdar, Local Coordinator, Indian Institute of Information Technology Design and Manufacturing, Jabalpur


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Mr./Ms. Gaikwad P.G. of SVERI'S COE, Pandharpur has participated in a one-week Faculty Development Programme on "VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies held from 16 – 20 December 2019, through National Knowledge Network under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology (MeitY), Government of India.

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Mr./Ms. Bhosale S.S. of SVERI'S COE, Pandharpur has participated in a one-week Faculty Development Programme on "VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies held from 16 – 20 December 2019, through National Knowledge Network under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology (MeitY), Government of India.

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Mr./Ms. Tambali H.M. of SVERI'S COE, Pandharpur has participated in a one-week Faculty Development Programme on "VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies held from 16 – 20 December 2019, through National Knowledge Network under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the Ministry of Electronics and Information Technology (MeitY), Government of India.

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Dr. Dip Prakash Samajdar, Local Coordinator, Indian Institute of Information Technology Design and Manufacturing, Jabalpur

B. C. Melinamath

Dr. B. C. Melinamath
HOD CSE